

WHAT IS CLAIMED IS:

1. A method comprising:

monitoring one or more sensor outputs of a sensor, the sensor to measure

a power consumption property of the chip, and each sensor output

to indicate a measurement of the power consumption property; and

recording a time that each of the one or more sensor outputs indicates an

existence of the power consumption property at a corresponding

measurement.
2. The method of claim 1, wherein the power consumption property of the

chip comprises temperature, and the temperature comprises a

temperature range including one or more temperatures.
3. The method of claim 2, wherein each sensor output corresponds to a

temperature range, and indicates the existence of the one or more

temperatures measured at the corresponding sensor output.
4. The method of claim 1, wherein the power consumption property of the

chip comprises voltage drop, and the voltage drop range includes one or

more voltage drops.
5. The method of claim 4, wherein each sensor output corresponds to a

voltage drop range, and each sensor output indicates the existence of a

voltage drop measured at the corresponding output.

6. A method comprising: /

obtaining event data, the event data including a time that each of one or

more sensor outputs indicates an existence of a power

consumption property at a corresponding measurement;

monitoring one or more parts of an application; and

for at least one of the one or more parts of the application, correlating the

event data with the at least one of the one or more parts of the

application.

7. The method of claim 6, wherein the power consumption property
comprises temperature.

8. The method of claim 6, wherein the power consumption property
comprises voltage drop.

9. An apparatus comprising:

circuitry capable of: /

obtaining event data, the event data including a time that each of

one or more sensor outputs indicates an existence of a

power consumption property at a corresponding

measurement;

monitoring one or more parts of an application; and

for at least one of the one or more parts of the application,

correlating the event data with the at least one of the one or more parts of the application.

10. The apparatus of claim 9, wherein the power consumption property of the chip comprises temperature, and the temperature comprises a temperature range including one or more temperatures.
11. The apparatus of claim 10, wherein each sensor output corresponds to a temperature range, and indicates the existence of the one or more temperatures measured at the corresponding sensor output.
12. The apparatus of claim 9, wherein the power consumption property of the chip comprises voltage drop, and the voltage drop comprises a voltage drop range including one or more voltage drops.
13. The apparatus of claim 12, wherein each sensor output corresponds to a voltage drop range, and each sensor output indicates the existence of a voltage drop measured at the corresponding output.
14. A system comprising:

circuitry on a first node, the circuitry connected to the chip and capable of:

obtaining event data, the event data including a time that each of

one or more sensor outputs indicates an existence of a

power consumption property at a corresponding

measurement;

monitoring one or more parts of an application; and

for at least one of one of the one or more parts of the application,
correlating the event data with the at least one of the one or
more parts of the application; and

a performance analyzer on a second node, the performance analyzer
communicatively coupled to the circuitry on the first node to use the
correlated information.

15. The system of claim 14, wherein the power consumption property of the chip comprises temperature, and wherein the temperature range includes one or more temperatures.
16. The system of claim 15, wherein each sensor output corresponds to a temperature range, and indicates the existence of the one or more temperatures measured at the corresponding sensor output.
17. The system of claim 14, wherein the power consumption property of the chip comprises voltage drop, and wherein the voltage drop range includes one or more voltage drops.
18. The system of claim 17, wherein each sensor output corresponds to a voltage drop range, and each sensor output indicates the existence of a voltage drop measured at the corresponding output.
19. An article comprising a machine-readable medium having machine-accessible instructions, the instructions when executed by a machine, result in the following:

obtaining event data, the event data including a time that each of one or more sensor outputs indicates an existence of a power consumption property at a corresponding measurement;

monitoring one or more parts of an application; and

for at least one of the one or more parts of the application, correlating the event data with the at least one of the one or more parts of the application.

20. The article of claim 19, wherein the power consumption property of the chip comprises a temperature range, and wherein the temperature range includes one or more temperatures.
21. The article of claim 20, wherein each sensor output corresponds to a temperature range, and indicates the existence of the one or more temperatures measured at the corresponding sensor output.
22. The article of claim 19, wherein the power consumption property of the chip comprises voltage drop range, and wherein the voltage drop range includes one or more voltage drops.
23. The article of claim 22, wherein each sensor output corresponds to a voltage drop range, and each sensor output indicates the existence of a voltage drop measured at the corresponding output.